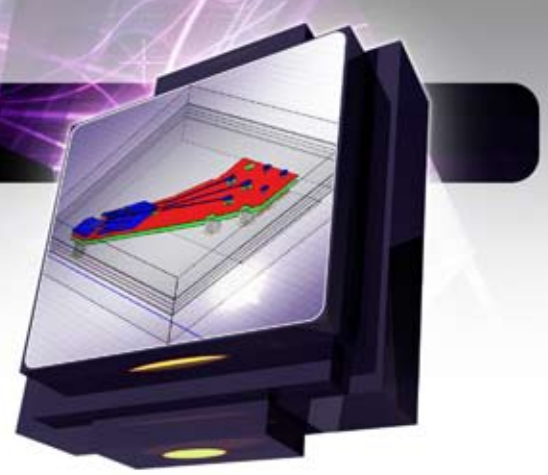


AnsoftLinks™

Design Automation for ECAD Layout

AnsoftLinks™ streamlines the transfer of design databases from popular third-party EDA layout tools into Ansoft electromagnetic field simulation products. EDA links are available for a number of tools offered by Cadence®, Zuken, Sigrity and Mentor Graphics®.

With AnsoftLinks, select components or complete IC, electronic, package, printed circuit board and connector designs can be automatically converted into ready-to-simulate Ansoft models, allowing engineers to use existing design geometries in their electromagnetic field simulation in order to meet aggressive development costs and schedule goals.



SUPPORTED ECAD LAYOUT SOFTWARE

Cadence Design Systems

- Allegro® PCB Design
- Allegro® Package Designer
- SiP Digital/RF
- Virtuoso®

Mentor Graphics

- BoardStation® and BoardStation® XE/RE
- Expedition™
- PADs Layout™

- Zuken CR-5000

- Sigrity UPD

FEATURED CAPABILITIES

- Generates ready-to-solve projects for HFSS™, Slwave™, Q3D Extractor®, Ansoft Designer®, and TPA™
- Unites touching metal into single conductor (including traces on multiple layers)
- Generates 3D solid models of selected traces, supply planes and vias
- Edit stack-up properties, such as individual layer thickness, dielectric constant and conductivity
- Select nets for translation by net name(s), or point and click on specific traces or region of the board
- Automatic de-featuring of selected nets to minimize model complexity and solution time
- IC Layout Specific
 - Exports specific “cells” (and their sub-cells)
 - Merge via arrays into a single object to simplify the solid model and speed analysis

- Packaging Layout Specific

- Generates 3D solid models for wire bonds, solder bumps, solder balls

APPLICATIONS

IC Packages

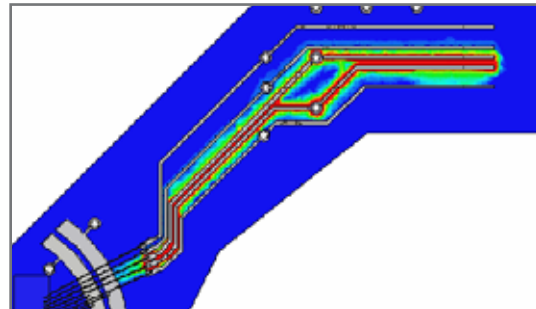
- Leaded-type (QFP, PLCC, DiP, SO)
- PGA
- BGA (wire-bonded, flip chip)
- MCM, SiP, LTCC

Printed Circuit Boards

- High-speed computer boards
- Network interface cards
- RF PCBs

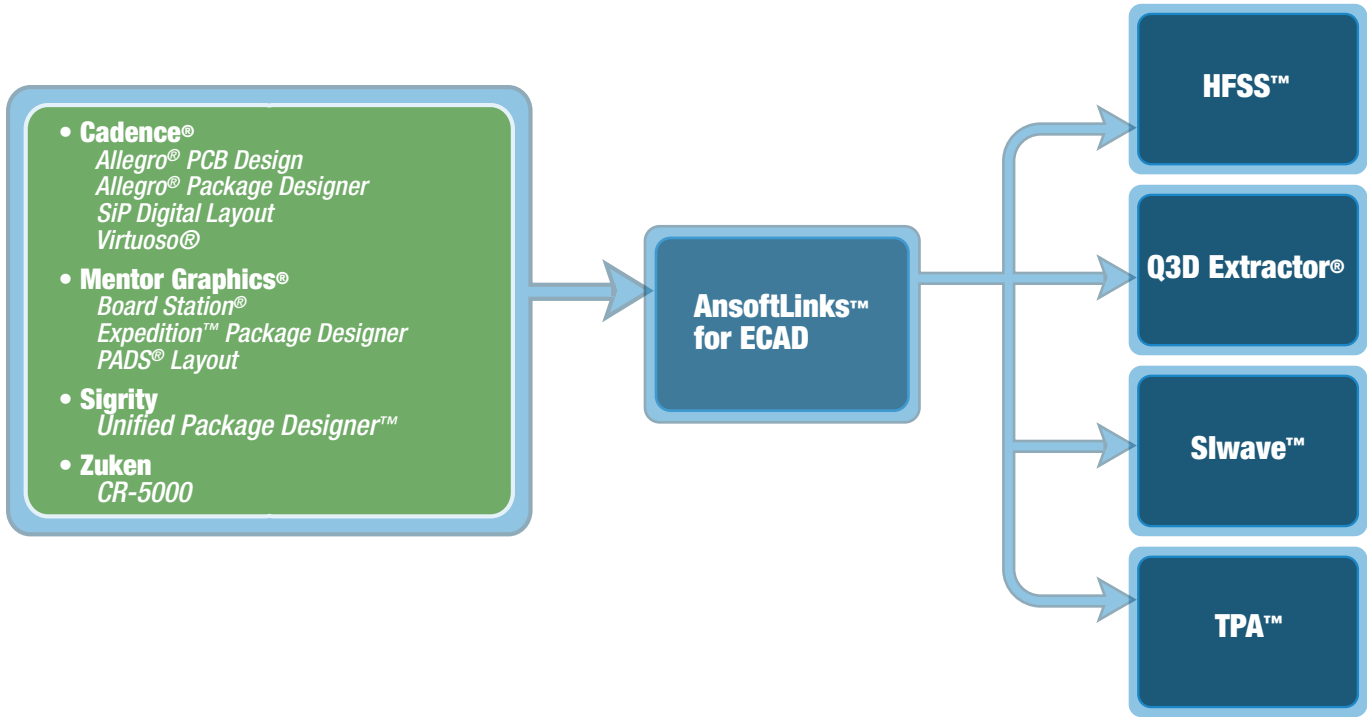
ICs

- Cells and sub-cells
- On-chip passives – vias, spiral inductors, interdigitated capacitors, interconnects



DESIGN FLOW

AnsoftLinks™ is critical for applications where design automation is necessary to bring together electromagnetic field simulation and the design entry and modeling tools required for capturing today's intricate design complexity. With AnsoftLinks, selected components in your IC, package, or printed circuit board design can be automatically translated and converted into ready-to-simulate models for the Ansoft electromagnetic field simulation software products, freeing the engineer from time-consuming and error-prone design re-entry.



A subsidiary of ANSYS, Inc.

225 West Station Square Drive • Suite 200
Pittsburgh, PA 15219-1119 USA

SIwave is a trademark of Ansoft, LLC.
All other trademarks are the property of their respective owners.
© 2009 Ansoft, LLC
MKT0000339 8-09