



*Training Seminar
for
Simplorer[®] 7.0*

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Training Seminar for Simplorer® 7.0

	<u>Day 1</u>	<u>Day 2</u>
9:00	Section 1: System Structure and Background Section 2: Circuit Simulation	Section 1: Subsheets and Macros Example Problem #1 <i>PWM Voltage Source - PWM.SSH</i> Section 2: Advanced Analysis Types Example Problem #2 <i>Parameter Sweep - ex02_Parameter Sweep.ssh</i>
10:45	Example Problem #1 <i>Varistor Non-Linear Resistance</i> <i>e01_nonlinear_resistor.SSH</i> Example Problem #2 <i>DC Drive for a Permanent Magnet</i> <i>DC Motor - ex02_dc_drive.SSH</i>	Example Problem #3 <i>Monte Carlo Analysis - ex03_Monte Carlo.ssh</i> Example Problem #4 <i>3D-Analysis - ex04_3D Analysis.ssh</i> Section 3: Scripting Example Problem #5 <i>Buck Converter Scripting with four cases</i>
12:00	LUNCH	LUNCH
13:00	Section 3: Block Diagram Simulation Example Problem #3 <i>Transfer Function Step Response</i> <i>ex03_transfer_function.SSH</i>	Section 4: Using several solvers with a Buck Converter Example Problem #6 <i>Transient solver with a "Switch Level model</i> <i>ex06_buck_tr_switch.ssh</i>
14:30	Section 4: State Machine Simulation Example Problem #4 <i>Single-Phase Inverter Bridge</i> <i>ex04_single_phase_inverter.SSH</i> Example Problem #5 <i>Online Measurement</i> <i>ex05_online_measurement.SSH</i> Example Problem #6 <i>Power Factor Correction</i> <i>ex06_PFC_with_Subsheet.ssh</i> Example Problem #7 <i>Extended Motor Example</i> <i>ex07_motor_extended.SSH</i> Section 6: Overview of Additional Libraries	Example Problem #7 <i>Transient solver with an "Average model</i> <i>ex07_buck_tr_average_topology.ssh</i> Example Problem #8 <i>AC solver with an "Average model</i> <i>ex08_buck_ac_average_topology.ssh</i> Example Problem #9 <i>AC solver for a Transistor Amplifier Topology</i> <i>ex09_transistor_amplifier_ac.ssh</i> Section 6: Multi-physical Domain Analysis Section 7: Improvements VHDL-AMS Section 8: Model Development Section 9: Component Dialog Wizard Section 10: C-Interface
17:00	Adjourn Section 7: Additional Information: AC-Analysis, VHDL-AMS, C-Interface, Matlab-Simulink-Interface	Adjourn